

## One - Page summary



### **Prof. Subir Kumar Sarkar**

IEEE Electron Device Society Distinguished Lecturer SMIEEE, Chartered Engineer,  
LFIETE, LFIE, LM ISTE, LM IACS, Coordinator of IC Design and Fabrication Centre  
& Coordinator of Evening Course for M Tech in VLSI Design  
Professor and Former Head

Department of Electronics and Telecommunication Engineering, Jadavpur  
University, Kolkata, India

E-mail: [su\\_sircir@yahoo.co.in](mailto:su_sircir@yahoo.co.in), [su\\_sarkar@hotmail.com](mailto:su_sarkar@hotmail.com), [sksarkar@etce.jdvu.ac.in](mailto:sksarkar@etce.jdvu.ac.in)



**Biography:** Prof. Subir Kumar Sarkar has completed his B. Tech, M. Tech and PhD (Tech) from Institute of Radiophysics and Electronics, University of Calcutta and Post Doctoral from Virginia Commonwealth University (VCU), USA. He has worked around 10 years in industry like Oil and Natural Gas Corporation (ONGC) as Executive Engineer, 25 years in Universities (8 Years in BESU and 17 Years in JU) in different capacities. He was the Head of the Department of Electronics and Telecommunication Engineering, Jadavpur University during 2011-2013, coordinator of the Evening course of M. Tech in “VLSI Design and Microelectronics Technology” 2009 -2013 & 2016- till date and Co-ordinator of IC Design & Fabrication Centre, Jadavpur University from 2016 to till date. He has authored 5 Engineering text books published by CRC Press USA, Artech House USA, PAN STANFOPRD USA, S.Chand& Company Pvt. Ltd., Inda. He has already guided 43 PhD scholars ( 8 more registered and currently working), 18 R&D projects sponsored by different Govt. of India funding agencies have been completed/ongoing, published more than 600 technical research papers in archived International/ National journals and peer reviewed conferences. His research areas include Nanoelectronic and photonic devices, SET, Spintronics devices and low power VLSI circuits, MEMs and NEMs, Gas sensors, Computer Netwrks (MANET and WSN), Digital Watermarking and RFID. He has visited several countries like USA, France, the United Kingdom, Switzerland, Japan, Thailand and Bangladesh as Keynote speaker, Special Guest of Honour, Invited speaker, for training, presenting papers and visiting sophisticated laboratories as a part of his collaborative research activities. He is a Senior Member of IEEE, IEEE EDS Distinguished Lecturer, Life fellow of IE (India) and IETE, Life member of ISTE and Life member of Indian Association for the Cultivation of Science (IACS). He has successfully organized two IEEE sponsored International Conferences as Convener (2004) and as General Chair (2012). He has filed one Indian Patent vide file No: 669/KOL/2013 dated 5<sup>th</sup> June, 2013.